

LAMINATED CERAMIC ELECTRONIC PARTSABSTRACT OF THE DISCLOSURE

There is provided a highly reliable laminated ceramic electronic part in which delamination or crack can be suppressed from occurring during a sintering process even if a number of lamination of internal electrodes is increased and a thickness of the ceramic layer is reduced and which excels in thermal shock resistance. The laminated ceramic electronic part is constructed so as to satisfy the following requirements of that a thickness of the ceramic layer is 10  $\mu\text{m}$  or less; a number of lamination of the internal electrodes is 200 or more; a ratio of a thickness of the internal electrode to the thickness of the ceramic layer (thickness of internal electrode/thickness of ceramic layer) is 0.10 to 0.40; and a ratio of a volume of the internal electrode to a volume of the ceramic element (volume of internal electrodes/volume of ceramic element) is 0.10 to 0.30.

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